PTO/SB/81 (09-04)

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Application Number	09/927,139	
Filing Date	August 10, 2001	
First Named Inventor	Gupta, Gagan V.	
Title	Floating Point Divide and Square	
Art Unit	2124	
Examiner Name	Ngo, Chuong D.	
Attorney Docket Number	01-074	

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Assignee of record of the entire interest. See 37 CFI Statement under 37 CFR 3.73(b) is efictosed. (Form						
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Signature	ure			Date	10-11-04	
Name Sandeep Jaggi				Telephone	(408) 954-4923	
Title and Company Chief IP Counsel, Assistant Corporate Secretary, LSI Logic Corporation						
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.						
*Total of forms are submitted.						

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: LSI Logic Corporation				
Application No./Patent No.: 09/927,139 Fi	iled/Issue Date: August 10, 2001			
Entitled: Floating Point Divide and Square Root Processor				
LSI Logic Corporation , a (Name of Assignee)	<u>Corporation</u> (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)			
states that it is: 1.  the assignee of the entire right, title, and interest;	or			
2. an assignee of less than the entire right, title and The extent (by percentage) of its ownership interest.	interest. est is %			
in the patent application/patent identified above by virtue	e of either:			
in the United States Patent and Trademark Office thereof is attached.	application/patent identified above. The assignment was recorded at Reel <u>012067</u> , Frame <u>0536</u> , or for which a copy			
B. A chain of title from the inventor(s), of the patent a below:	application/patent identified above, to the current assignee as shown			
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2. From:	To:			
The document was recorded in the United Reel , Frame	States Patent and Trademark Office at, or for which a copy thereof is attached.			
The document was recorded in the United Reel, Frame	To:			
Additional documents in the chain of title are lie	sted on a supplemental sheet.			
Copies of assignments or other documents in the ch [NOTE: A separate copy (i.e., a true copy of the orig Division in accordance with 37 CFR Part 3, if the MPEP 302.08]	nain of title are attached.  Iginal assignment document(s)) must be submitted to Assignment ne assignment is to be recorded in the records of the USPTO. See			
The undersigned (whose lite is supplied below) is author	orized to act on behalf of the assignee.			
) Signature	Date			
Sandeep Jaggi, Reg. No. 43,331				
Printed or Typed Name	Telephone Number			
Chief IP Counsel and Assistant Corporate Se	cretary			

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